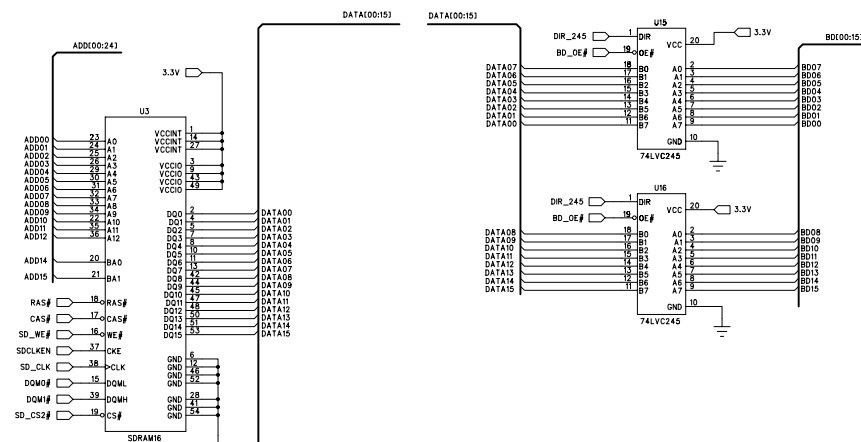
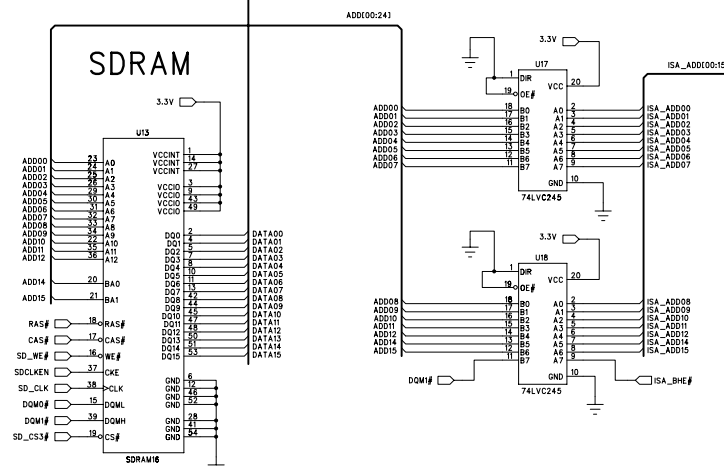


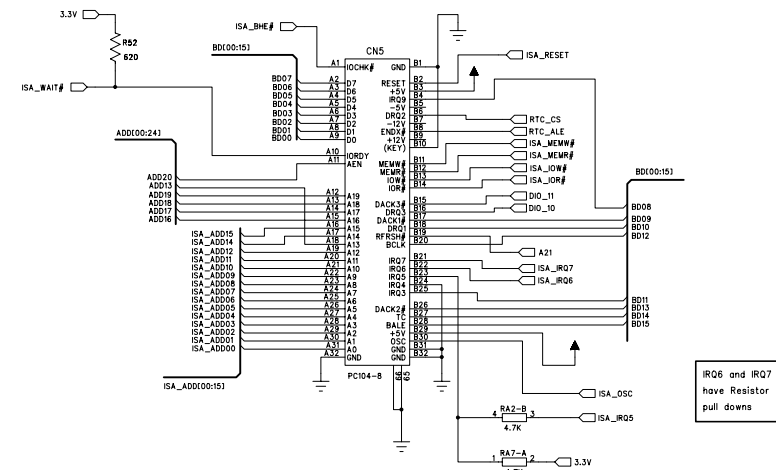
SDRAM



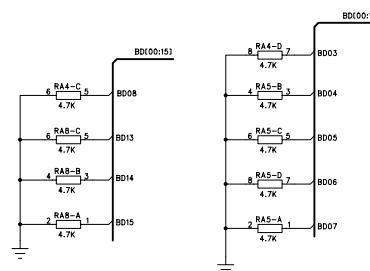
SDRAM



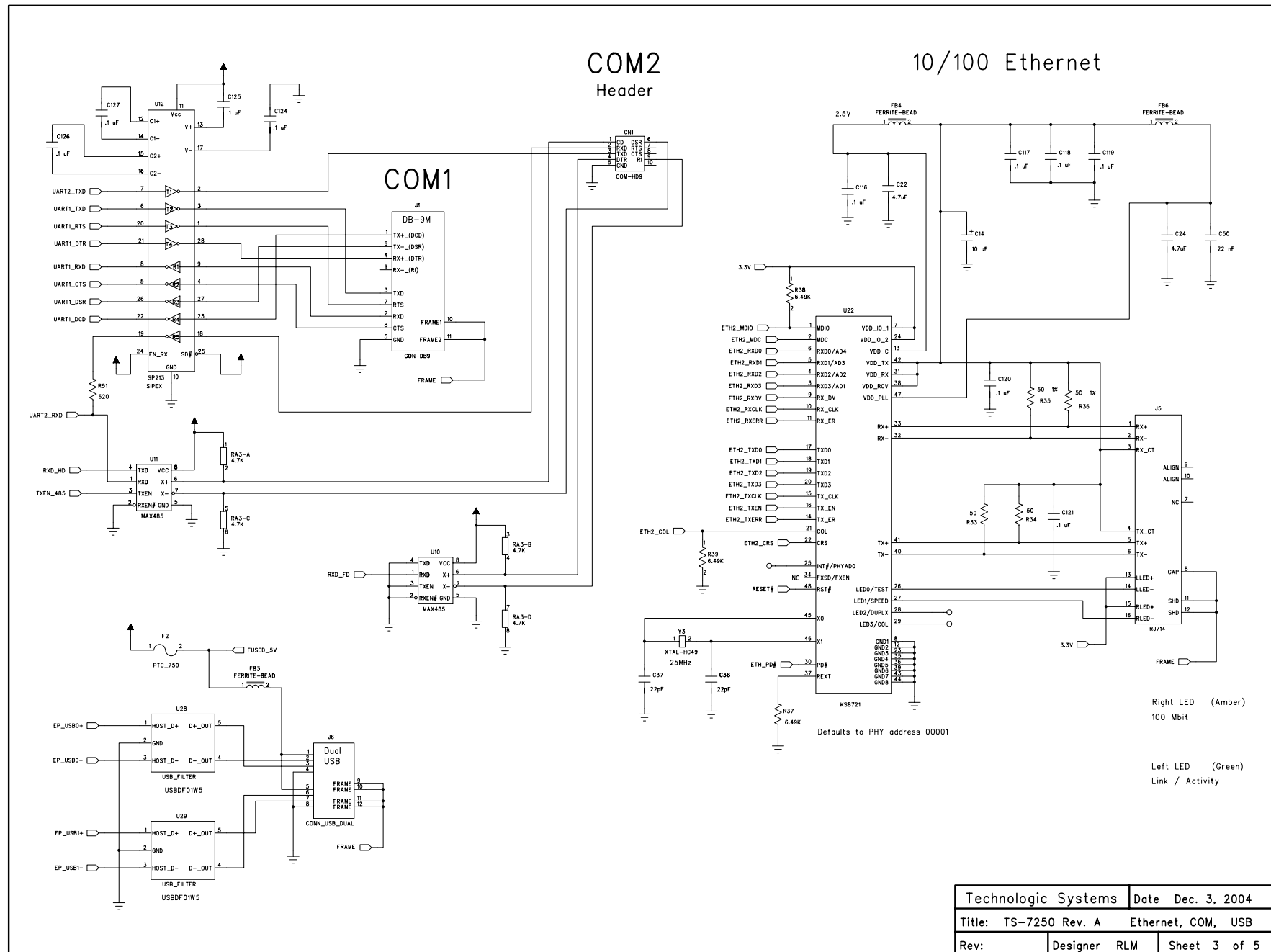
PC/104 Connector



Data Bus
Pull-Down
Resistors

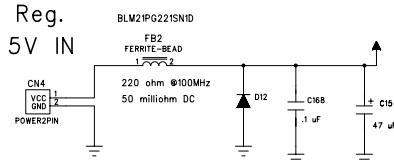


Technologic Systems	Date	Dec. 3, 2004
Title:	TS-7250 Rev. A RAM, Flash, PC/104	
Rev:	Designer RLM	Sheet 2 of 5



Technologic Systems	Date	Dec. 3, 2004
Title:	TS-7250 Rev. A	Ethernet, COM, USB
Rev:	Designer RLM	Sheet 3 of 5

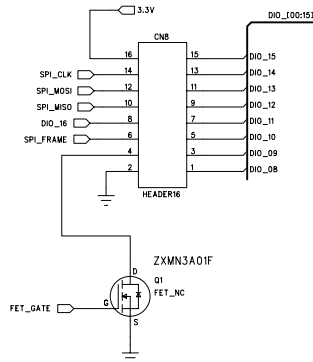
Reg.
5V IN



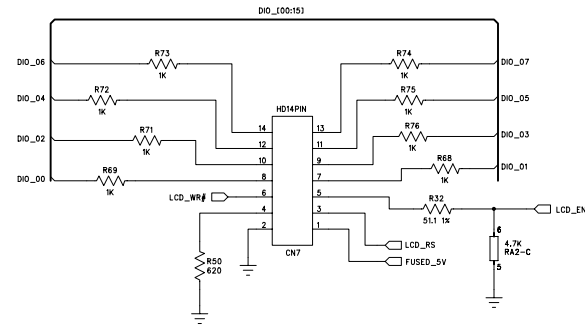
Jumpers:

- JP1 = Boot Serial
- JP2 = Console Enable
- JP3 = Write Enable Flash
- JP4 = Stop @ Redboot
- JP5 = TS_Test
- JP6 = Reserved

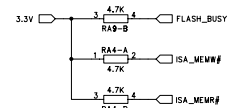
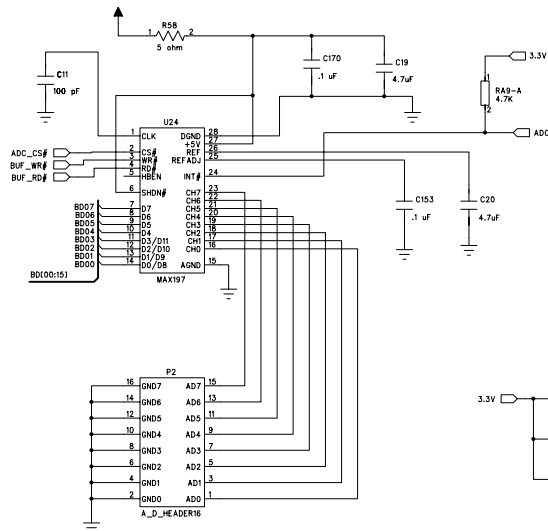
DIO Port



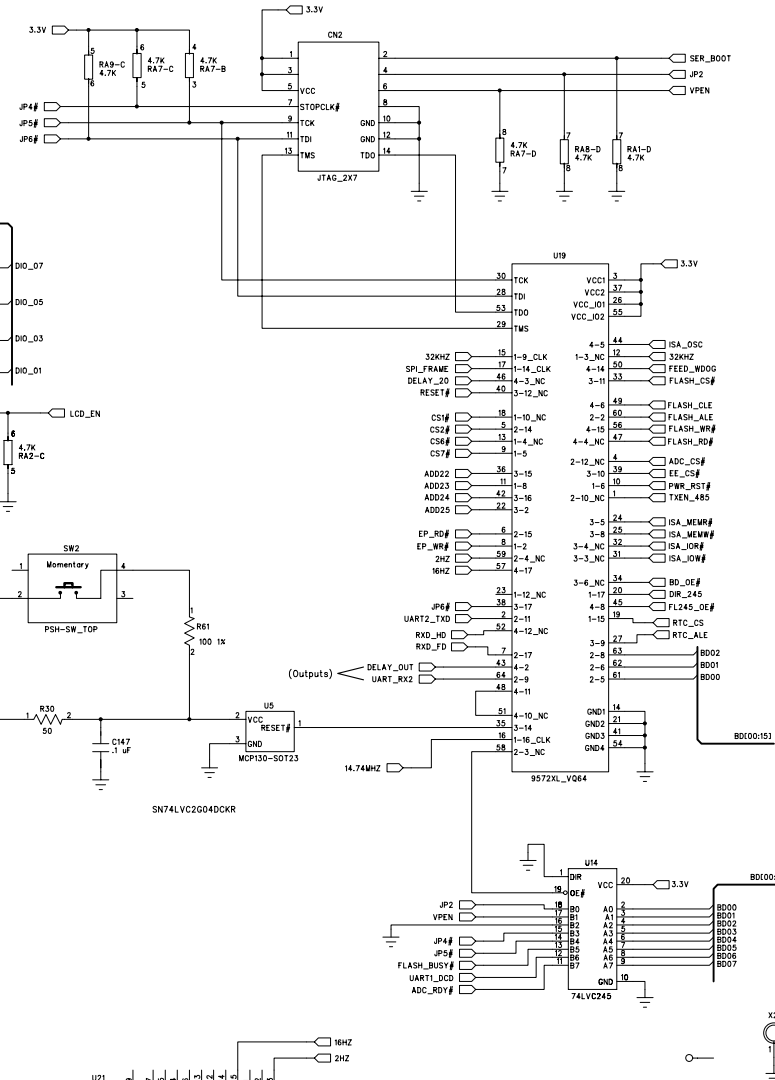
LCD Port



12-bit A/D Converter

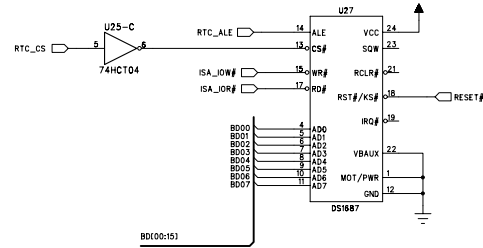


JTAG

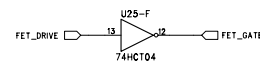
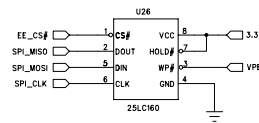
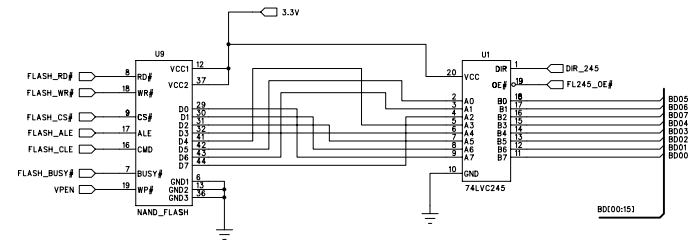


Technologic Systems	Date March 17, 2008
Title: TS-7250 Rev. A	PLD, LCD, DIO, ADC
Rev:	Designer
	Sheet 4 of 5

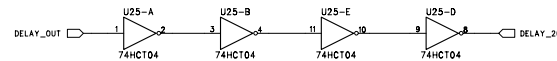
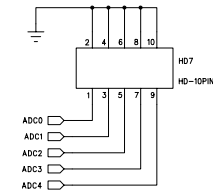
Real Time Clock



NAND Flash



5 Channel 12-bit A/D



Technologic Systems	Date March 5, 2005
Title: TS-7250 Rev. A NAND Flash, EEPROM, RTC	
Rev: 1.0	Designer RLM Sheet 5 of 5